Programme
ASTP-Proton Site Visit to IMEC
13 October 2014
Leuven - Belgium

09:30 **Welcome and registration**

10:00 **IMEC introduction (Anne Van den Bosch and Benny Graindourze)**
While very well known for its international IMEC Industrial Affiliation Programs in Semiconductor Scaling, IMEC offers much more research topics (like photovoltaics, bio-electronics, telecommunication) and uses also other innovation models (like funded programs) while ensuring a close link to the local community.
In this introduction a total (once lightly over) picture of IMEC (budgets, infrastructure) will be provided in an attempt to show the total ambit of IMEC.

10:45 **IMEC IP model (Vincent Ryckaert)**
The above defined context (heterogeneous topics, many different technology readiness levels) obviously requires an integrated IP approach. In this presentation the IMEC tool set (R&D life cycle, TRL, Business models, IP stack) and terminology will be described in more detail. The purpose is to indicate how IMEC bridges the gap between Academia and Industry and also some observed trends and challenges in this Open IP and Open Innovation landscape.

11:30 **IMEC venturing (Johan Haspeslagh)**
Spin-off creation (one of the many business models) is a very special one. The specialists of the venturing team will explain the flow and (financial) instruments supporting the spin-off creation.

12:15 – 13:30 **Lunch break**

13:30 **Visit IMEC cleanroom (Chris Van Herck & Jan Potemans)**

14:00 **Parallel workshops IP**
The group splits in two groups to facilitate a more interactive debate:

*Workshop A: CORE CMOS: Supplier (Ilse Heyvaert) & IIAP (*Vincent Ryckaert)
In Workshop A focus will be given to the Semiconductor Scaling research topic and the delicate balance between End-users and Suppliers but also adaptations due to the consolidation in industry will be worked out.

*Workshop B: MORE THAN MOORE (*Katrien Meuwis & Kathleen De Belder)
In Workshop B focus will be given to the other research topics – and especially development on demand and low volume manufacturing but also IP monetization insights will be explained.

14:45 **Coffee/ tea break**

15:00 **Parallel Workshops IP A (CORE CMOS: Supplier), B (MORE THAN MOORE)**
Workshop A: CORE CMOS: Supplier (Ilse Heyvaert) & IIAP (Vincent Ryckaert).
Workshop B: MORE THAN MOORE (Katrien Meuwis & Kathleen De Belder)

15:45 **Conclusions, Q&A**

16:30 **End of the day**